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Fundamentals of Digital Logic with Verilog Design

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Chapter 2 2.1. The proof is as follows: $(x + y) \cdot (x + z) = xx + xz + xy + yz = x + xz + xy + yz = x(1 + z + y) + yz = x \cdot 1 + yz = x + yz$ 2.2.

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fundamentals of digital logic and microcomputer design. Danh mục: Đại cương. ... from a basic point of view. Logic-level design is the design technique in which logic gates are used to design a digital component such as an adder. Finally, system-level design is covered ...

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